

Claims:

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1. An RGB control circuit for use in television/video display control, comprising:
a display driver current sensor;
5 a counter circuit and analog output circuit coupled to control the display driver current;
a speeding comparator having a plurality of comparator circuits coupled in parallel with the display driver current sensor as input, for determining and outputting a measure of the difference between the sensed display driver current and a predetermined value thereof;
10 and
a speeding logic circuit coupled to the speeding comparator and counter circuit, and arranged to control the up/down counting rate of the counter circuit according to said measure of difference in display driver current.
- 15 2. An RGB control circuit as claimed in claim 1, wherein the speeding logic circuit is arranged to control the counting rate of the counter circuit, and thus the display driver current, based on the output of the speeding comparator so as to converge the display driver current to said predetermined value.
- 20 3. An RGB control circuit as claimed in claim 2, wherein said speeding logic circuit produces a RGB output blanking signal whilst said display driver current is substantially different from said predetermined value.
4. An RGB control circuit as claimed in claim 3, including control circuits for each of
25 the colour channels, and wherein said speeding logic circuit produces said RGB output blanking signal based on the counting rate of any and/or each of the counter circuits for the colour channels.
5. An RGB control circuit as claimed in claim 1 or 2, wherein the speeding comparator
30 has a plurality of outputs including a convergent output and at least one upper output and

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lower output, and wherein said convergent output corresponds to said display driver current being substantially equal to said predetermined value and said upper and lower outputs are utilised by said speeding logic circuit to determine the up/down counting rate of the counter circuit.

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6. An RGB control circuit as claimed in claim 5, wherein each of said upper and lower outputs correspond to respective up and down binary counting rates for said counter circuit.

7. An RGB control circuit as claimed in claim 1 or 2, wherein the up/down counter and
10 analog output circuit are both 9-bit circuits.

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